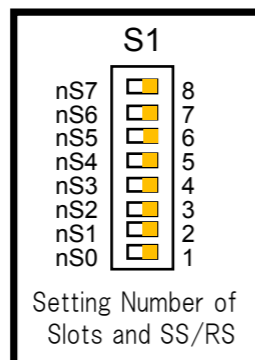


**Active Low**

(1) Setting number of slots per cycle

RG20 series allow 4, 8, 16, 32, 64 and 128 slots per cycle. Following is the table of number of slots per cycle setting.

Slot数	nS7	nS6	nS5	nS4	nS3	nS2	nS1	nS0
4	L	L	L	L	L	H	*	*
6	L	L	L	L	H	*	*	*
16	L	L	L	H	*	*	*	*
32	L	L	H	*	*	*	*	*
64	L	H	*	*	*	*	*	*
128	H	*	*	*	*	*	*	*



(2) Setting SS ( Sending Slot)

Pins in lower (right hand) order than the first pin from the left with value "H" are used to specify SS (Sending Slot) Number. For example, if the number of slots per cycle is "4", the first pin from the left with value "H" is nS2. So nS1 and nS0 are used to specify SS from 0 to 3.

SS	nS7	nS6	nS5	nS4	nS3	nS2	nS1	nS0
0	L	L	L	L	L	H	H	H
1	L	L	L	L	L	H	H	L
2	L	L	L	L	L	H	L	H
3	L	L	L	L	L	H	L	L

(3) Setting RS ( Receiving Slot)

RS is always the value of SS with inverted MSB.

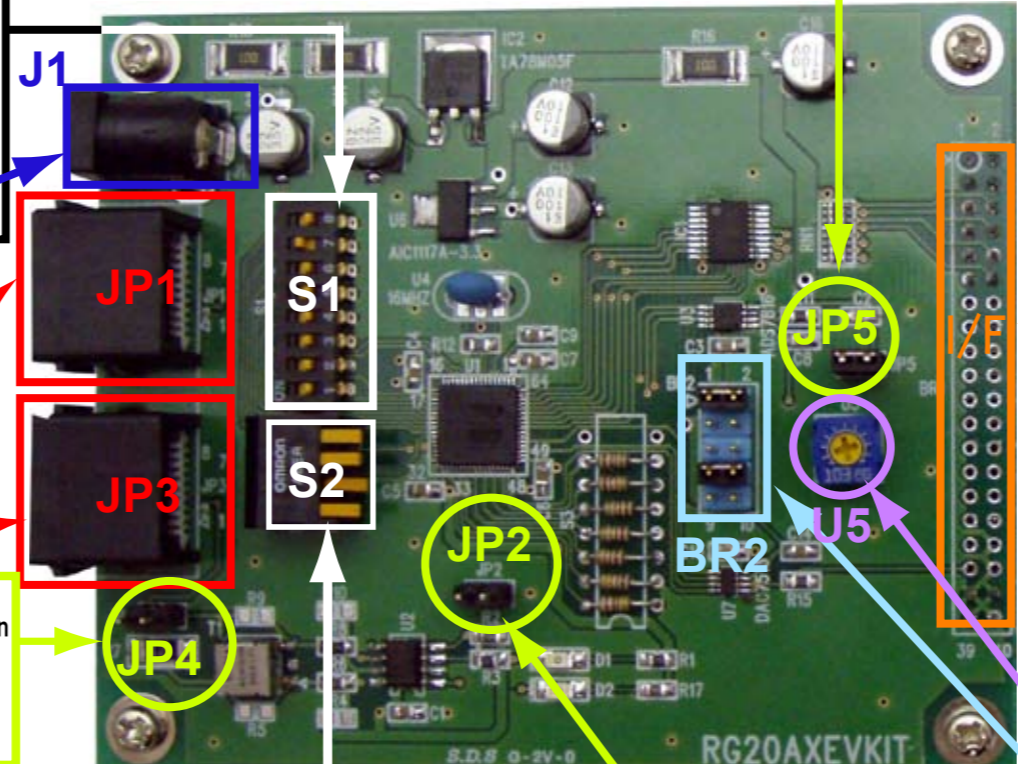
SS	RS
0	2
1	3
2	0
3	1

**JP5 Analog volume connection setting**

JP5	Var. Resister
Open	Disable
Short	Enable

General I/F MIL40

Use	PIN	PIN	Use
12V	1	2	12V
12V	3	4	12V
nDOT15	5	6	nDIN15
nDOT14	7	8	nDIN14
nDOT13	9	10	nDIN13
nDOT12	11	12	nDIN12
N/C	13	14	N/C
N/C	15	16	N/C
N/C	17	18	N/C
N/C	19	20	N/C
N/C	21	22	N/C
N/C	23	24	N/C
N/C	25	26	N/C
N/C	27	28	N/C
N/C	29	30	N/C
N/C	31	32	N/C
ADIN	33	34	GND
DAOUT	35	36	GND
GND	37	38	GND
GND	39	40	GND



**J1 Power supply**

Supply DC12V

PIN	1	2	3
Use	0V	OPEN	12V

**JP1,JP3 RJ45 Connector for RiB serial line**

Although the connector is same with the ethernet (LAN), they have different electric characteristics, so DO NOT connect other ethernet devices to this connector.

PIN	1	2	3	4	5	6	7	8
Use	12V	12V	A	N/C	N/C	B	GND	GND

**JP4 RiB Serial termination**

JP4	Terminal
Open	No
Short	Yes

**JP2 OUTG Setting**

- (4) HOST (HostMode Enable)
- (5) nHOLD

**JP2 OUTG Setting**

(4)HOST	(5)nHOLD	(JP3)OUTG	
H	H	Open	MC mode(Within talker and listener function)
	L	Short	Listener mode(High noise resistance setting)
L	H	Open	Talker & Listener mode
	L	Short	Out of use

**Active Low**

(1) nHL

Communication method compatibility with HL10V/HL11V series.

When nHL is L, some of newly added methods for better reliability are disabled and RG20 can communicate with preceding RiB devices,

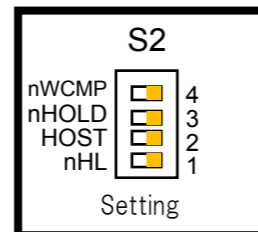
nHL	HL10V/HL11compatibility
L	Compatible
H	Incompatible

(4) nWCMP

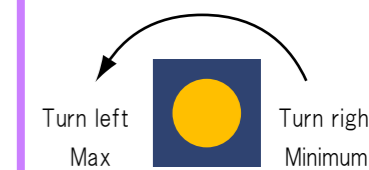
Multi-cycle Data Verification

When nWCMP (double compare) is "L", RG20 compares data received from the serial line with the data of previous cycle, and RG20 accepts data only when both data matches. This feature is designed for applications which require high data reliability or applications under noisy environments.

nWCP	Multi-cycle Data Verification
L	ON
H	OFF

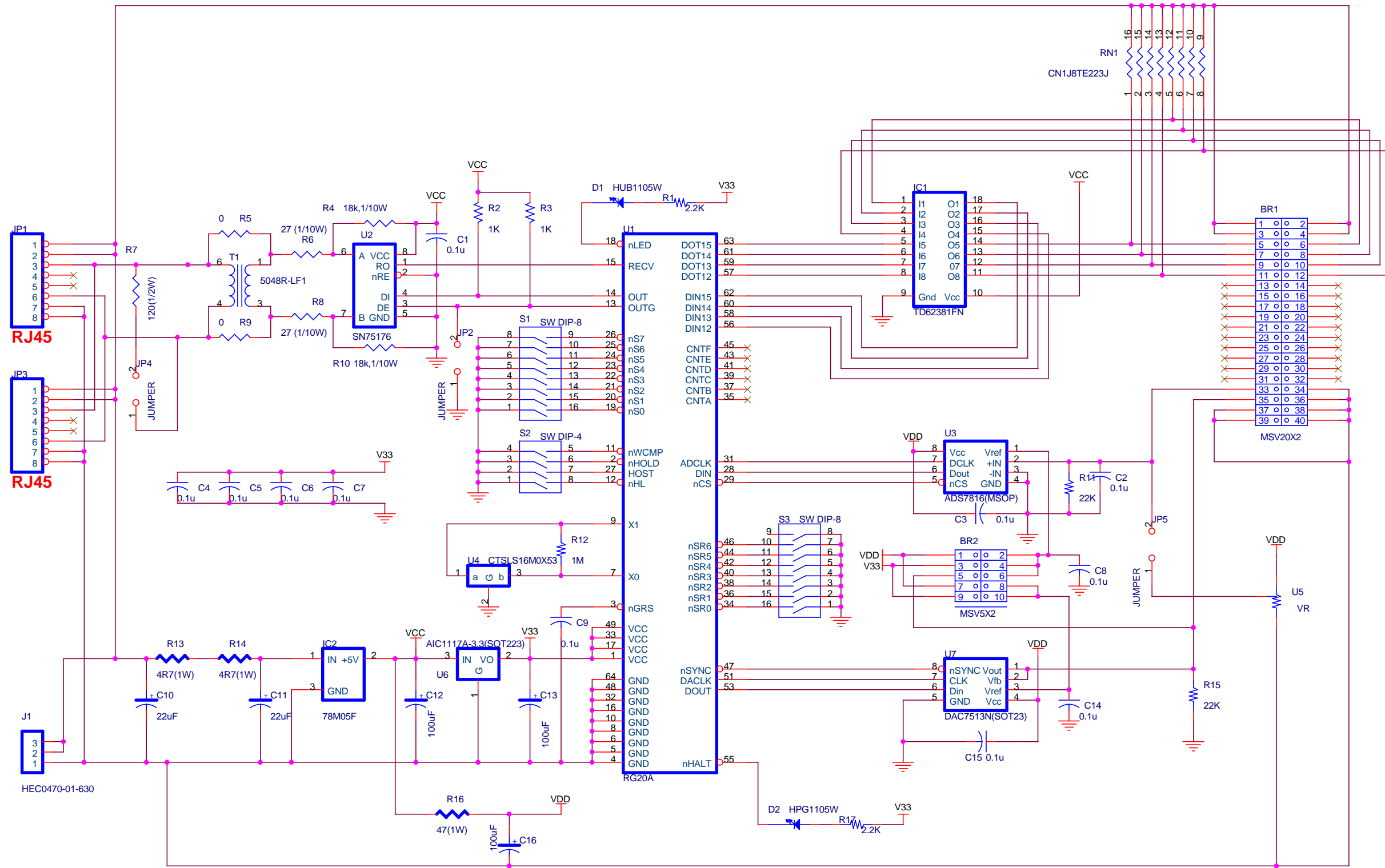


**U5 Analog input volume setting**



**BR2 Reference voltage of ADC, DAC setting**

1	2	••	VDD(5V)	} ADC Vref
3	4	••	V33(3.3V)	
5	6	••	Vout(variable)	} DAC Vref
7	8	••	VDD(5V)	
9	10	••	V33(3.3V)	



### RG20 アナログ入出力モジュール

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Title		
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A3	<Doc>	1.0
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