



DIP Converter for Evaluate RG20

| | |
|-------------------|---------------------|
| Size | 81.28 × 25.4 mm |
| Supply Voltage | 3.3 V |
| Control points | Input16 / Output 16 |
| Output Bus | RiB |
| Input Buffer | 16 bit |
| Output Buffer | 16 bit |
| Enegy Comsumption | max. 100mW |
| Connector Pin | 64pin Dip |

RG20DIP64 PIN LIST

MODE1 (Output Positive Logic) is Usable. Reference the RG20 Manual.

MODE0 Output Negative Logic

| | | | | | | | |
|----|--------|-----|--|----|--------|-----|-------------------------|
| 1 | VCC | VCC | VCC | 33 | VCC | VCC | VCC |
| 2 | nHOLD | IN | Low : Keep Data , High : Reset at Com. Error | 34 | nDIN02 | IN | Input : Negative Logic |
| 3 | nGRS | IN | Reset (Negative Logic) | 35 | DOT02 | OUT | Output : Negative Logic |
| 4 | GND | GND | GND | 36 | nDIN03 | IN | Input : Negative Logic |
| 5 | GND | GND | GND | 37 | DOT03 | GND | Output : Negative Logic |
| 6 | nMD0 | IN | VCC | 38 | nDIN04 | IN | Input : Negative Logic |
| 7 | X0 | OUT | Quarts Crystal Unit A | 39 | DOT04 | OUT | Output : Negative Logic |
| 8 | GND | GND | GND | 40 | nDIN05 | IN | Input : Negative Logic |
| 9 | X1 | IN | Quarts Crystal Unit B | 41 | DOT05 | OUT | Output : Negative Logic |
| 10 | nMD1 | IN | VCC | 42 | nDIN06 | IN | Input : Negative Logic |
| 11 | nWCMP | IN | GND : WCMP ON , VCC : Normal Mode | 43 | DOT06 | OUT | Output : Negative Logic |
| 12 | nHL | IN | HL10/11 Compatible Mode | 44 | nDIN07 | IN | Input : Negative Logic |
| 13 | OUTG | OUT | RS485 Output Gate | 45 | DOT07 | OUT | Output : Negative Logic |
| 14 | OUT | OUT | RS485 Output | 46 | nDIN08 | IN | Input : Negative Logic |
| 15 | RECV | IN | RS485 Input | 47 | DOT08 | OUT | Output : Negative Logic |
| 16 | GND | GND | GND | 48 | GND | GND | GND |
| 17 | VCC | VCC | VCC | 49 | VCC | VCC | VCC |
| 18 | nLED | OUT | Normal : Blinking, WDT : Light | 50 | nDIN09 | IN | Input : Negative Logic |
| 19 | nS0 | IN | Slot/Cycle & Slot Number Setting | 51 | DOT09 | OUT | Output : Negative Logic |
| 20 | nS1 | IN | Slot/Cycle & Slot Number Setting | 52 | nDIN10 | IN | Input : Negative Logic |
| 21 | nS2 | IN | Slot/Cycle & Slot Number Setting | 53 | DOT10 | OUT | Output : Negative Logic |
| 22 | nS3 | IN | Slot/Cycle & Slot Number Setting | 54 | nDIN11 | IN | Input : Negative Logic |
| 23 | nS4 | IN | Slot/Cycle & Slot Number Setting | 55 | DOT11 | OUT | Output : Negative Logic |
| 24 | nS5 | IN | Slot/Cycle & Slot Number Setting | 56 | nDIN12 | IN | Input : Negative Logic |
| 25 | nS6 | IN | Slot/Cycle & Slot Number Setting | 57 | DOT12 | OUT | Output : Negative Logic |
| 26 | nS7 | IN | Slot/Cycle & Slot Number Setting | 58 | nDIN13 | IN | Input : Negative Logic |
| 27 | HOST | IN | GND : MC OFF, VCC : MC ON | 59 | DOT13 | OUT | Output : Negative Logic |
| 28 | nDIN00 | IN | Input : Negative Logic | 60 | nDIN14 | IN | Input : Negative Logic |
| 29 | DOT00 | OUT | Output : Negative Logic | 61 | DOT14 | OUT | Output : Negative Logic |
| 30 | nDIN01 | IN | Input : Negative Logic | 62 | nDIN15 | IN | Input : Negative Logic |
| 31 | DOT01 | OUT | Output : Negative Logic | 63 | DOT15 | OUT | Output : Negative Logic |
| 32 | GND | GND | GND | 64 | GND | GND | GND |

MODE3 ANALOG MODE

Connectable ADC is ADS7816, connectable AC is DAC7513

| | | | | | | | |
|----|-------|-----|--|----|-------|-----|--|
| 1 | VCC | VCC | VCC | 33 | VCC | VCC | VCC |
| 2 | nHOLD | IN | Low : Keep Data , High : Reset at Com. Error | 34 | nSR0 | IN | Slew Rate Limiter [bit4] |
| 3 | nGRS | IN | Reset (Negative Logic) | 35 | CNTA | OUT | Sent Data Counter [A] |
| 4 | GND | GND | GND | 36 | nSR1 | IN | Slew Rate Limiter [bit5] |
| 5 | GND | GND | GND | 37 | CNTB | GND | Sent Data Counter [B] |
| 6 | nMD0 | IN | GND | 38 | nSR2 | IN | Slew Rate Limiter [bit6] |
| 7 | X0 | OUT | Quarts Crystal Unit A | 39 | CNTC | OUT | Sent Data Counter [C] |
| 8 | GND | GND | GND | 40 | nSR3 | IN | Slew Rate Limiter [bit7] |
| 9 | X1 | IN | Quarts Crystal Unit B | 41 | CNTD | OUT | Sent Data Counter [D] |
| 10 | nMD1 | IN | GND | 42 | nSR4 | IN | Slew Rate Limiter [bit8] |
| 11 | nWCMP | IN | GND : WCMP ON , VCC : Normal Mode | 43 | CNTE | OUT | Sent Data Counter [E] |
| 12 | nHL | IN | HL10/11 Compatible Mode | 44 | nSR5 | IN | Slew Rate Limiter [bit9] |
| 13 | OUTG | OUT | RS485 Output Gate | 45 | CNTF | OUT | Sent Data Counter [F] |
| 14 | OUT | OUT | RS485 Output | 46 | nSR6 | IN | Slew Rate Limiter [bit10] |
| 15 | RECV | IN | RS485 Input | 47 | nSYNC | OUT | DAC --- [nSYNC] |
| 16 | GND | GND | GND | 48 | GND | GND | GND |
| 17 | VCC | VCC | VCC | 49 | VCC | VCC | VCC |
| 18 | nLED | OUT | Normal : Blinking, WDT : Light | 50 | GND | IN | |
| 19 | nS0 | IN | Slot/Cycle & Slot Number Setting | 51 | DACLK | OUT | DAC --- [DACLK] |
| 20 | nS1 | IN | Slot/Cycle & Slot Number Setting | 52 | GND | IN | |
| 21 | nS2 | IN | Slot/Cycle & Slot Number Setting | 53 | DOUT | OUT | DAC --- [DOUT] |
| 22 | nS3 | IN | Slot/Cycle & Slot Number Setting | 54 | GND | IN | |
| 23 | nS4 | IN | Slot/Cycle & Slot Number Setting | 55 | nHALT | OUT | WDT動作時 Low |
| 24 | nS5 | IN | Slot/Cycle & Slot Number Setting | 56 | DIN12 | IN | Input : Positive Logic(Status Information) |
| 25 | nS6 | IN | Slot/Cycle & Slot Number Setting | 57 | DOT12 | OUT | Output : Positive Logic(Control Information) |
| 26 | nS7 | IN | Slot/Cycle & Slot Number Setting | 58 | DIN13 | IN | Input : Positive Logic(Status Information) |
| 27 | HOST | IN | GND : MC OFF, VCC : MC ON | 59 | DOT13 | OUT | Output : Positive Logic(Control Information) |
| 28 | DIN | IN | ADC --- [DIN] | 60 | DIN14 | IN | Input : Positive Logic(Status Information) |
| 29 | nCS | OUT | ADC --- [nCS] | 61 | DOT14 | OUT | Output : Positive Logic(Control Information) |
| 30 | GND | IN | | 62 | DIN15 | IN | Input : Positive Logic(Status Information) |
| 31 | ADCLK | OUT | ADC --- [ADCLK] | 63 | DOT15 | OUT | Output : Positive Logic(Control Information) |
| 32 | GND | GND | GND | 64 | GND | GND | GND |